

FIG. 1

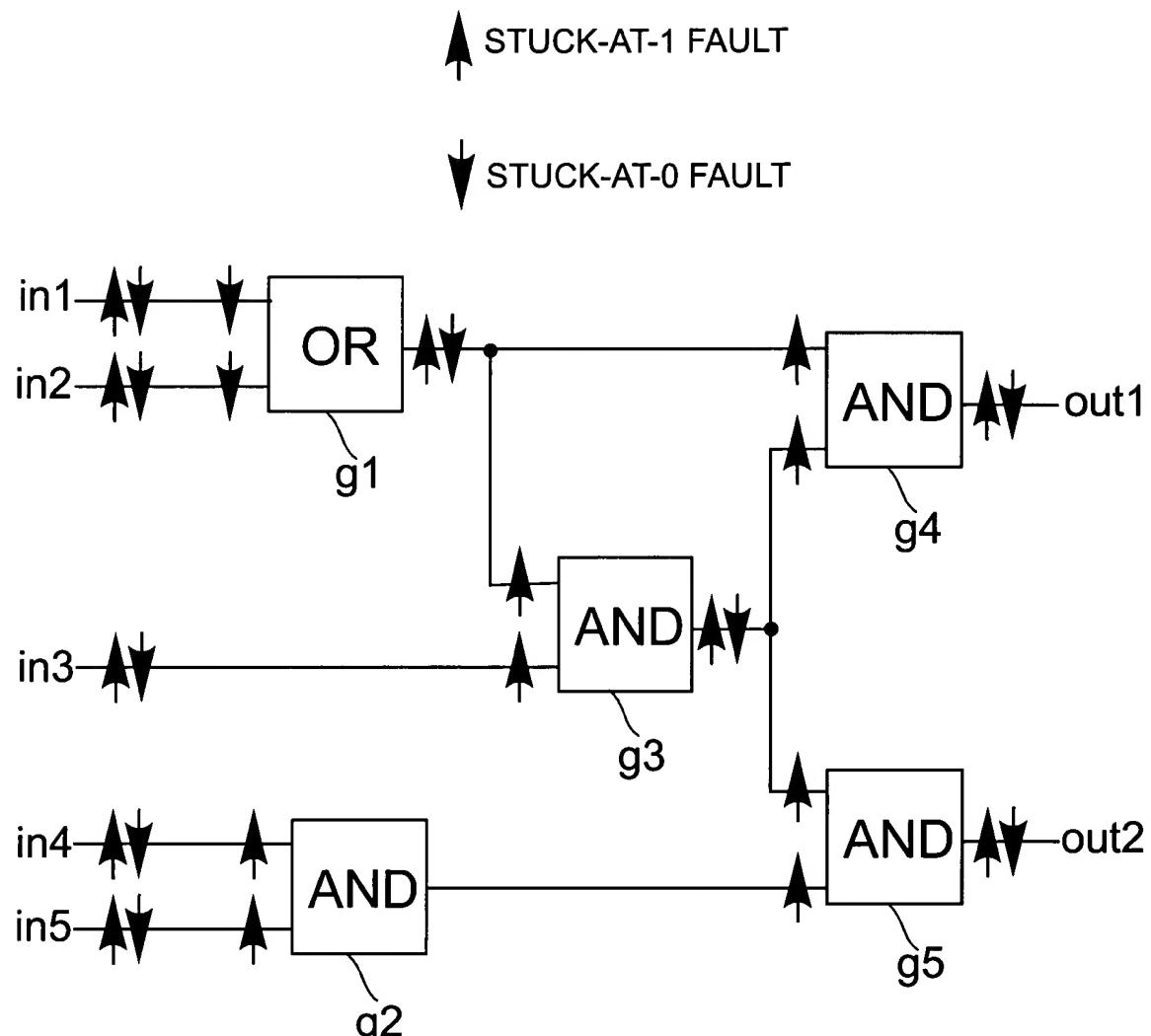


FIG. 2

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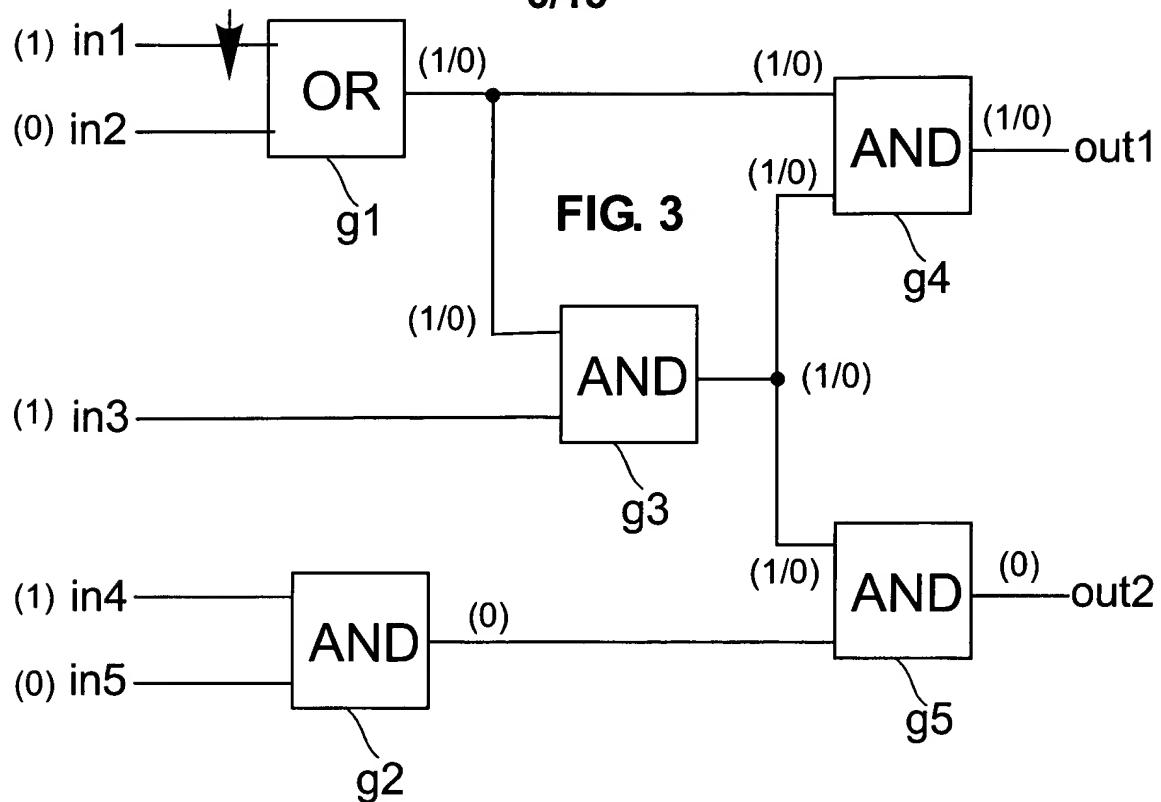
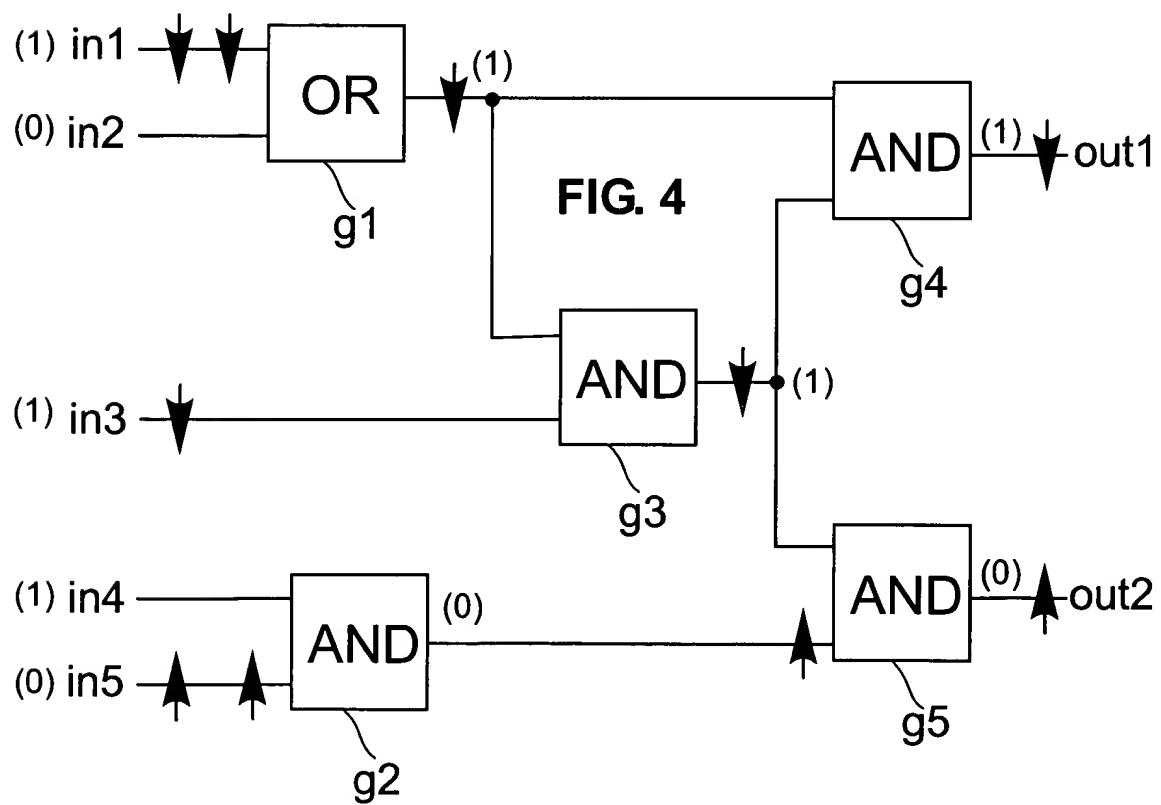
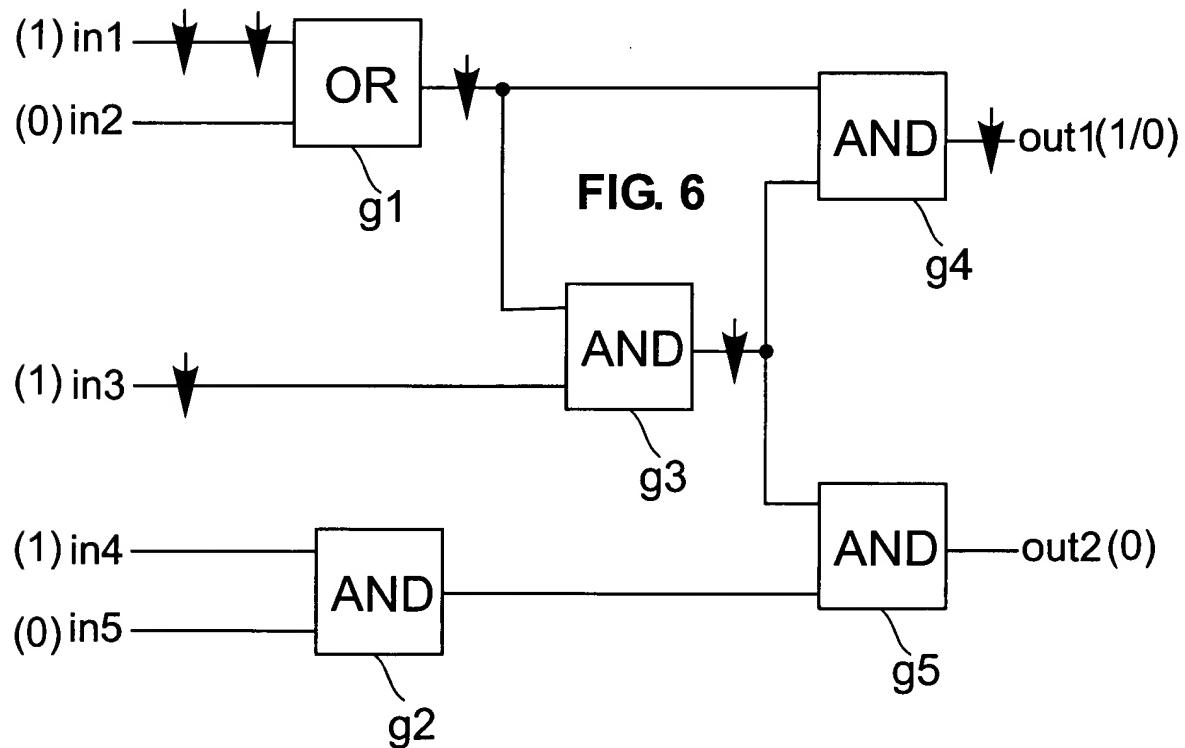
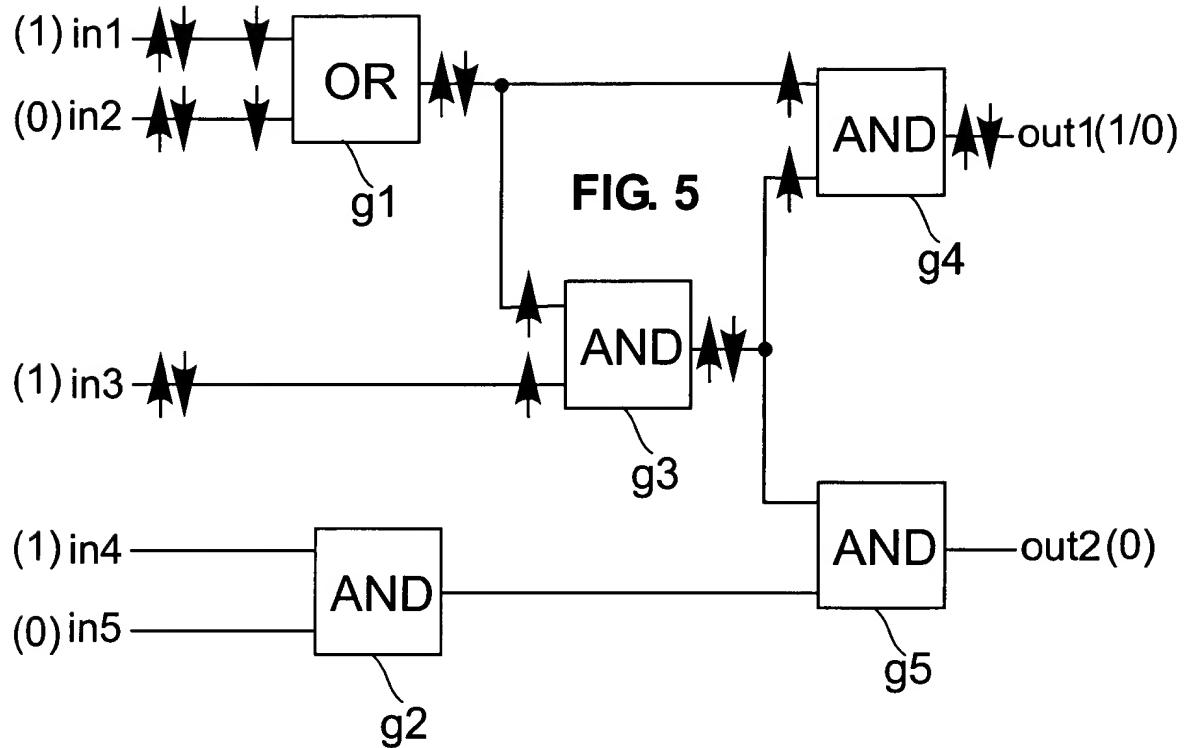


FIG. 4





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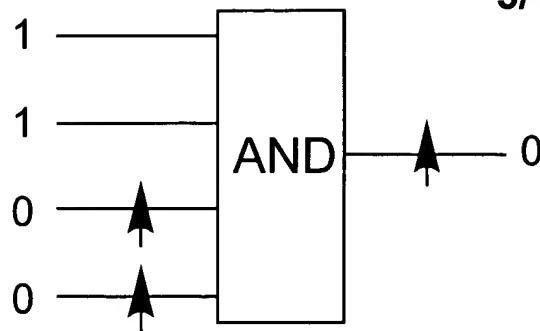


FIG. 7(a)

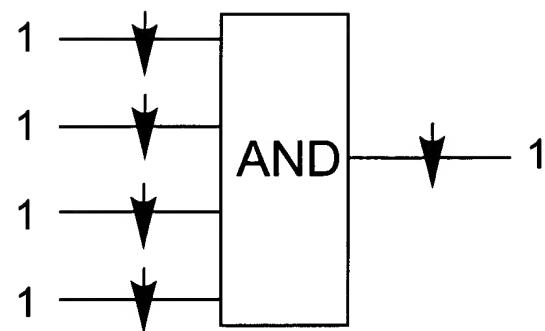


FIG. 7(b)

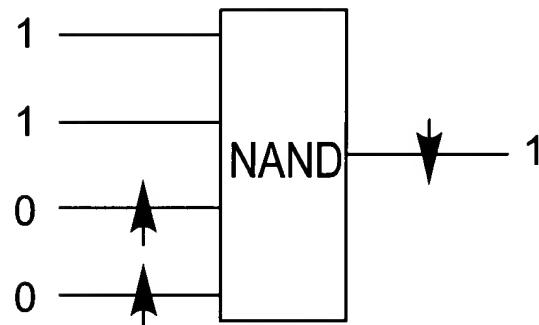


FIG. 7(c)

↑↑ COLLAPSED
STUCK-AT FAULTS
↑↑ NON-COLLAPSED
STUCK-AT FAULTS

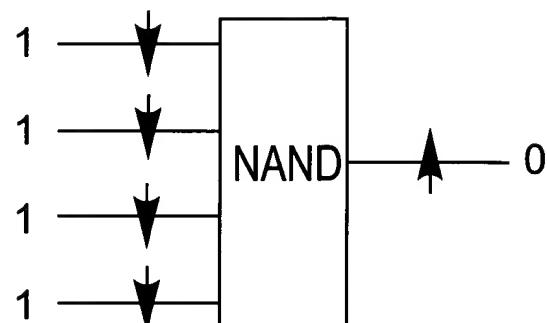


FIG. 7(d)

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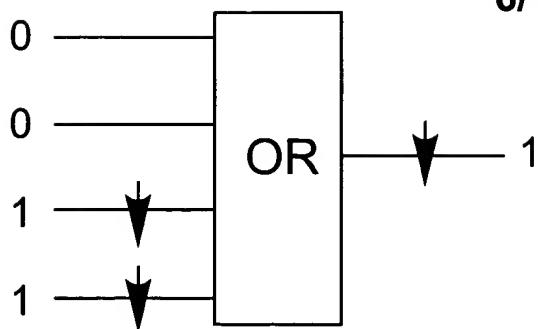


FIG. 8(a)

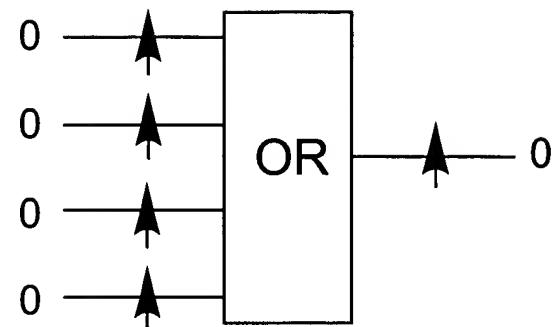


FIG. 8(b)

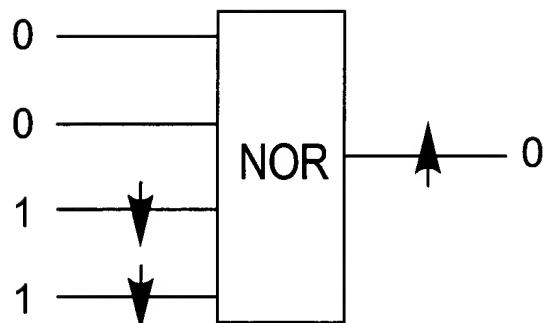


FIG. 8(c)

↑↑ COLLAPSED
STUCK-AT FAULTS
↑↑ NON-COLLAPSED
STUCK-AT FAULTS

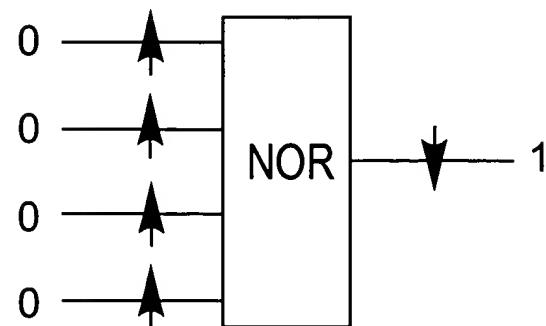
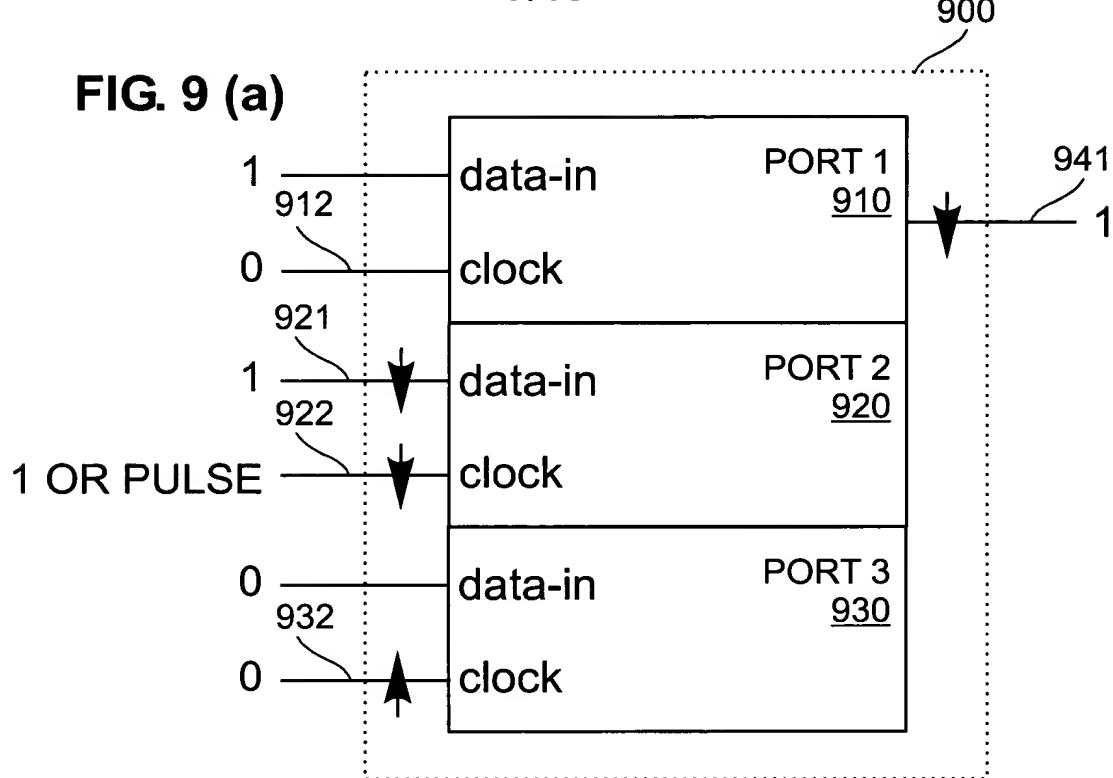
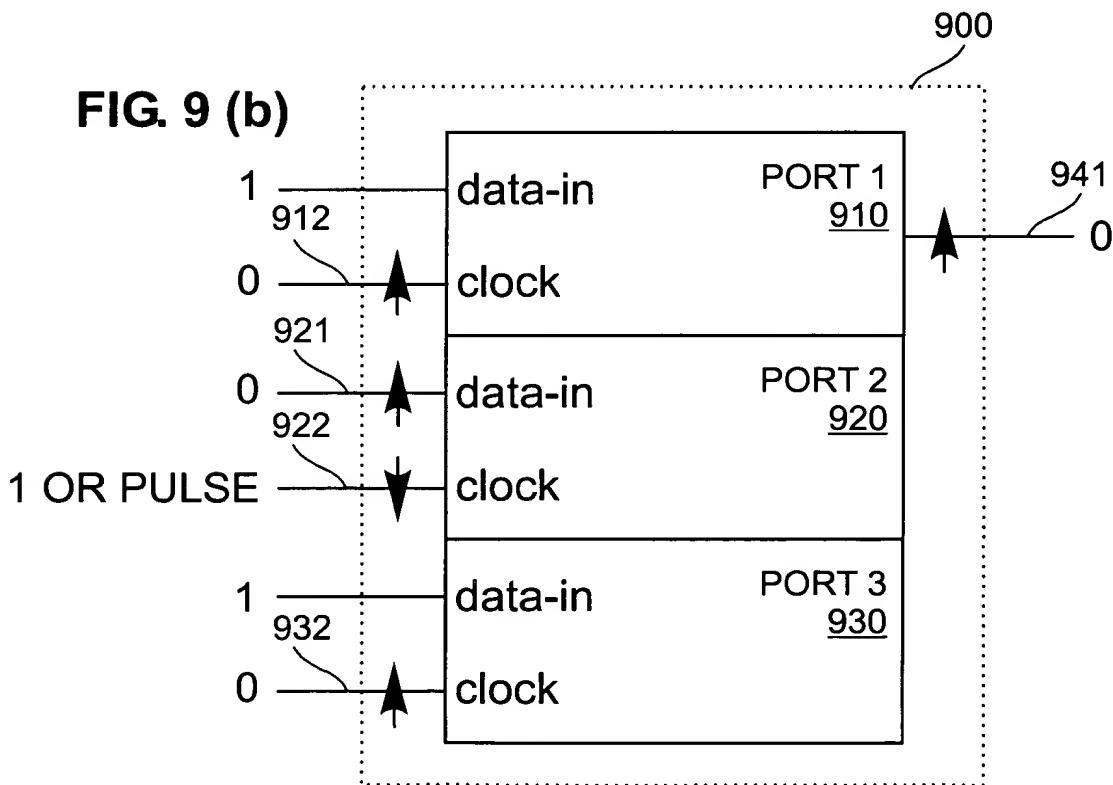


FIG. 8(d)

FIG. 9 (a)**FIG. 9 (b)**

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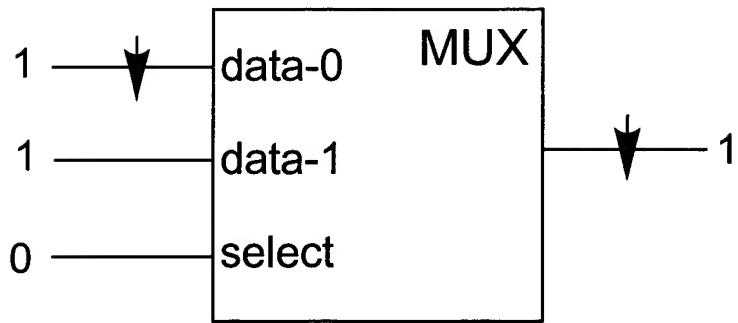


FIG. 10(a)



FIG. 10(b)



FIG. 10(c)

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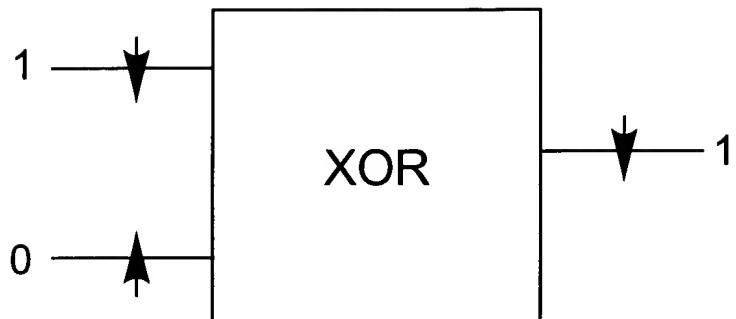


FIG. 11(a)

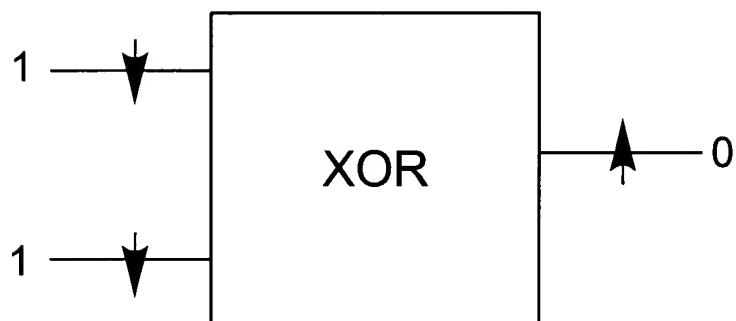


FIG. 11(b)

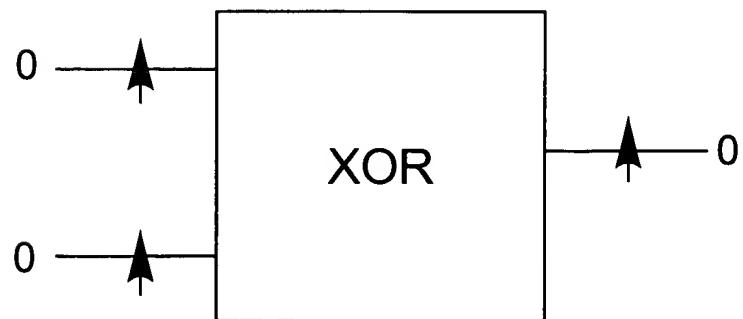


FIG. 11(c)

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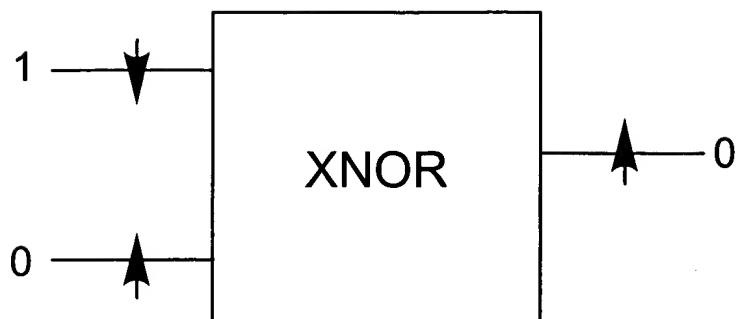


FIG. 12(a)

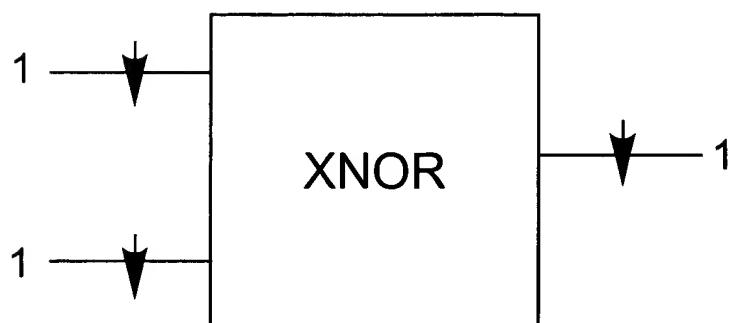


FIG. 12(b)

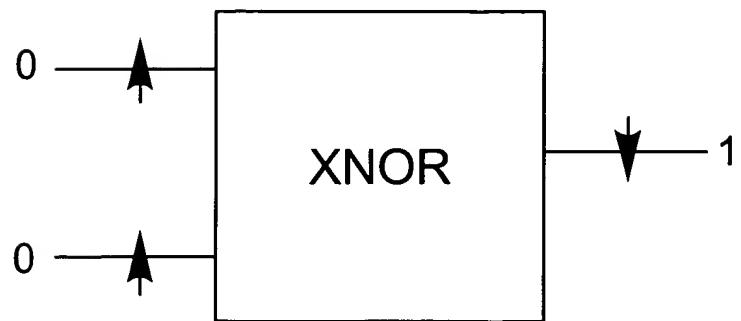


FIG. 12(c)

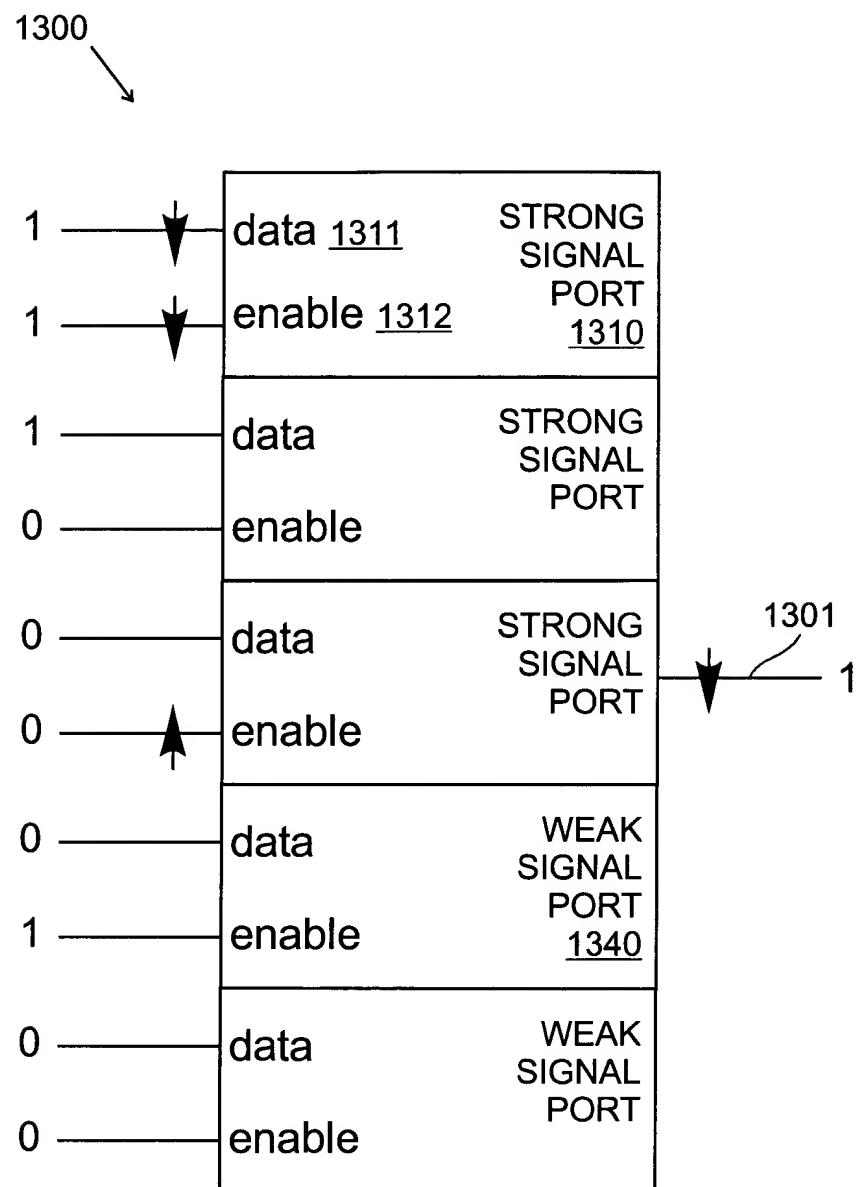


FIG. 13

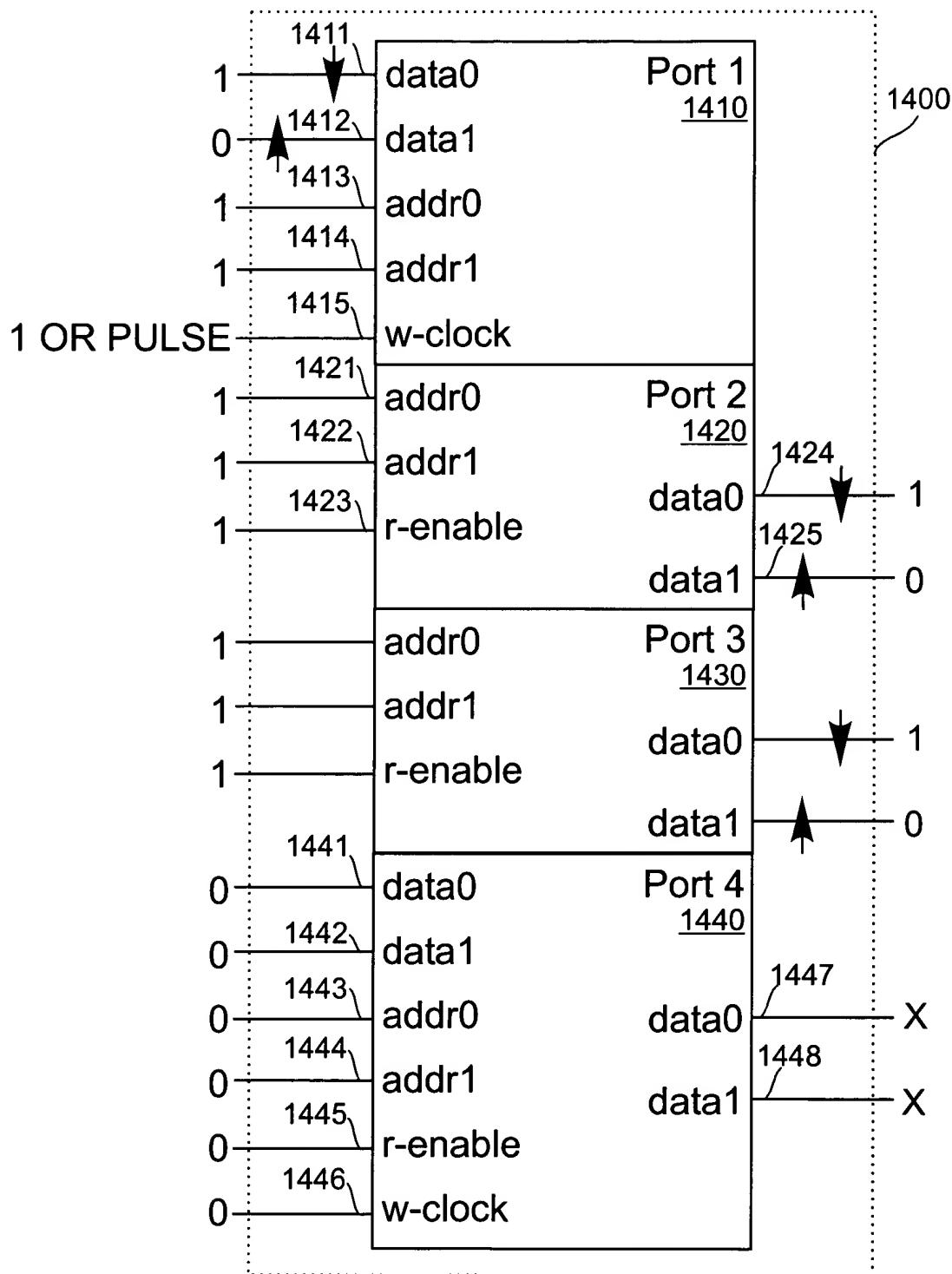


FIG. 14

ROM CONTENT TABLE

addr0	addr1	bit0	bit1
0	0	0	0
0	1	0	1
1	0	1	0
1	1	1	1

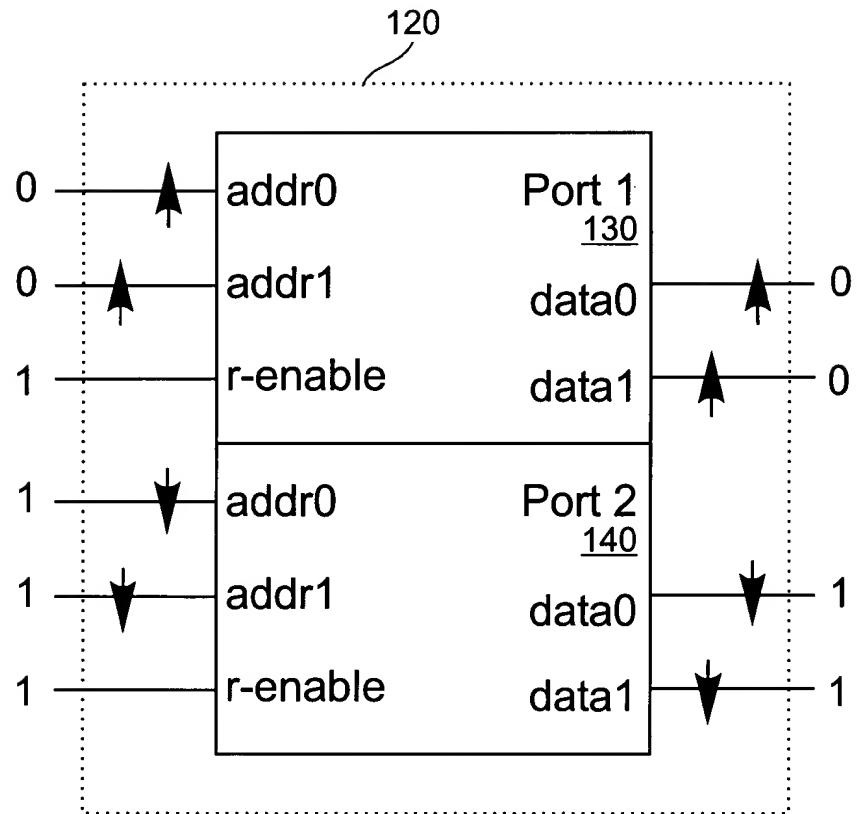


FIG. 15

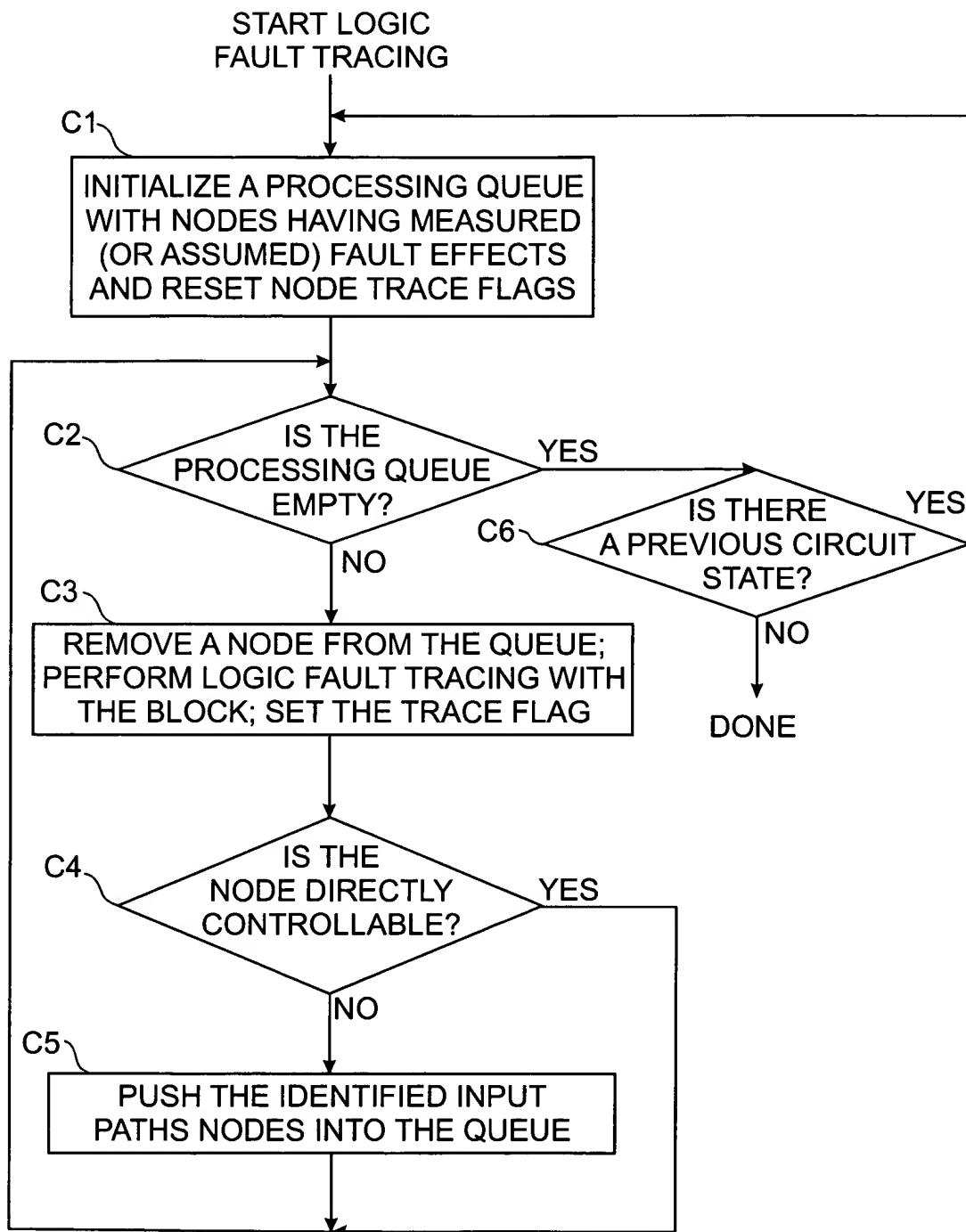


FIG. 16

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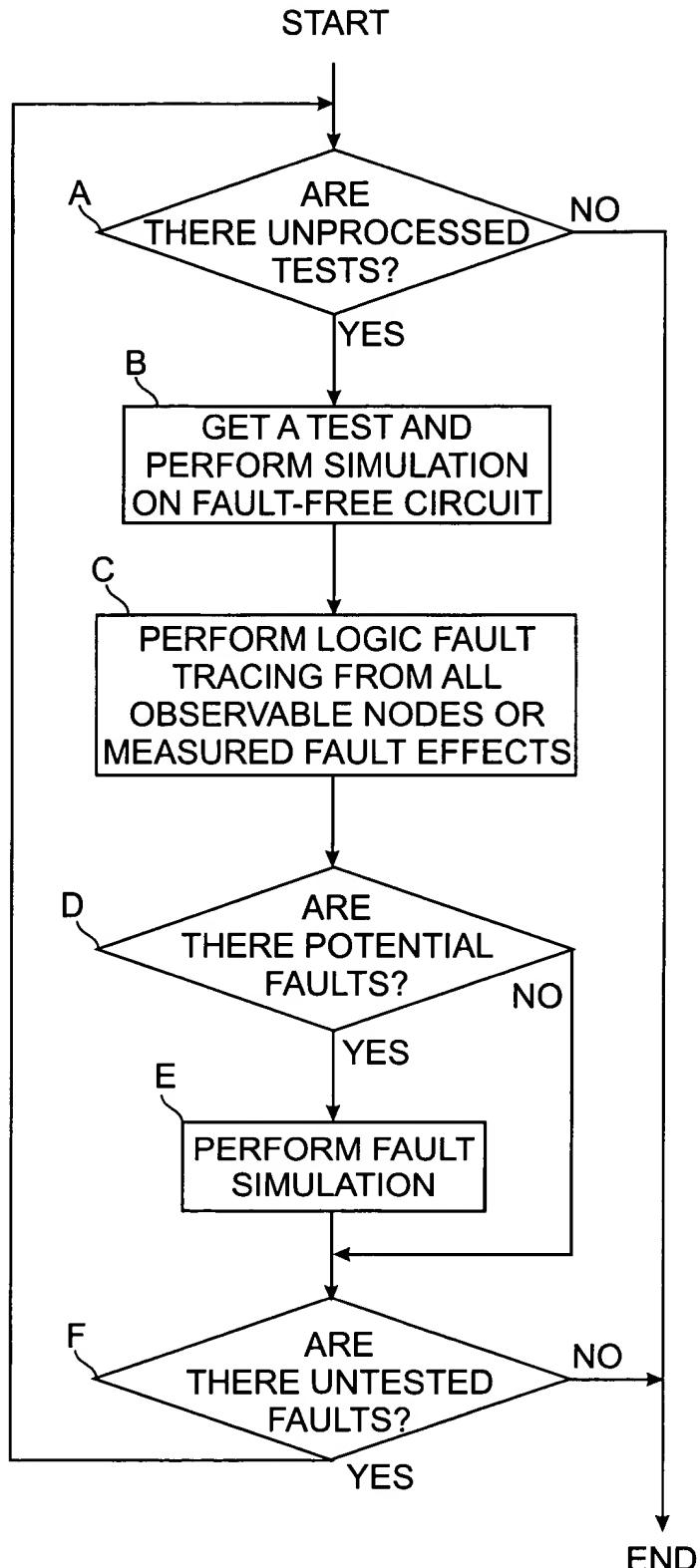


FIG. 17